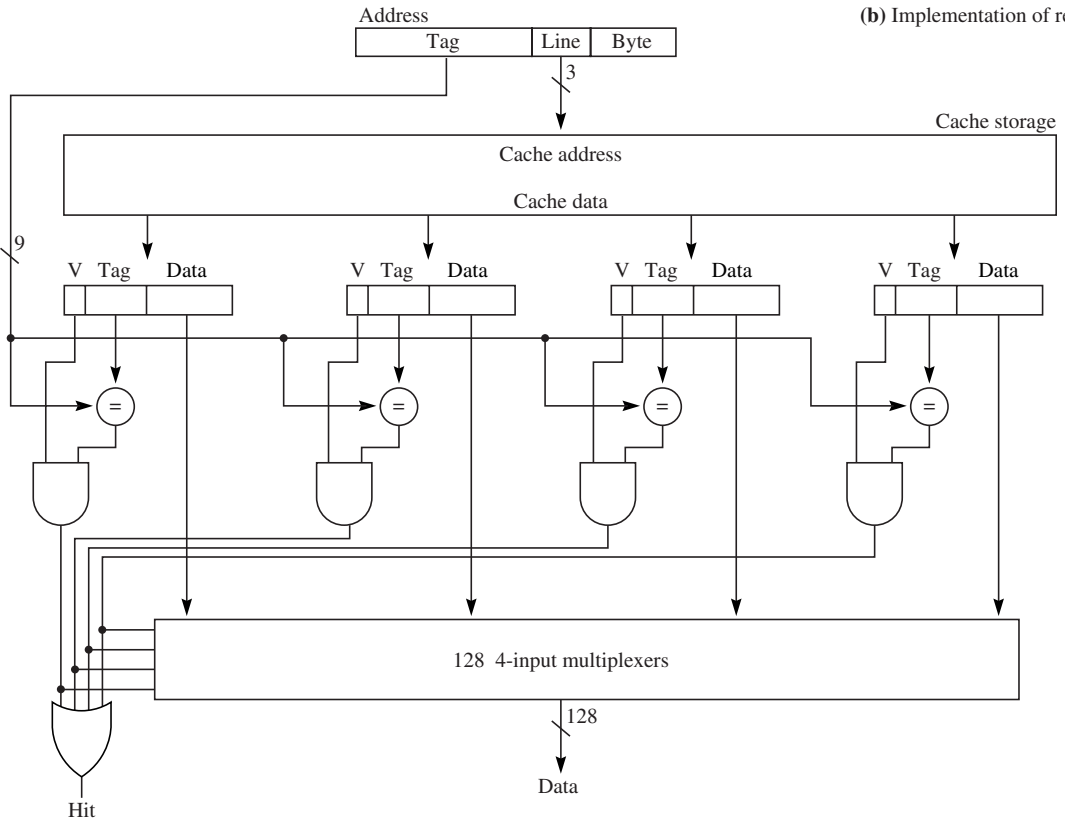


(a) Block diagram of cache storage.



(b) Implementation of read circuit.