

Addressing mode	Size of OprndSpec	Operand
Immediate	16 bits	OprndSpec
Register	5 bits	Reg [OprndSpec]
Base	5 bits and 16 bits	Mem [Reg[OprndSpec1] + OprndSpec2]
PC-relative	16 bits	Mem [(PC + 4) + OprndSpec * 4]
Pseudodirect	26 bits	Mem [(PC + 4) <0..3> : OprndSpec * 4]