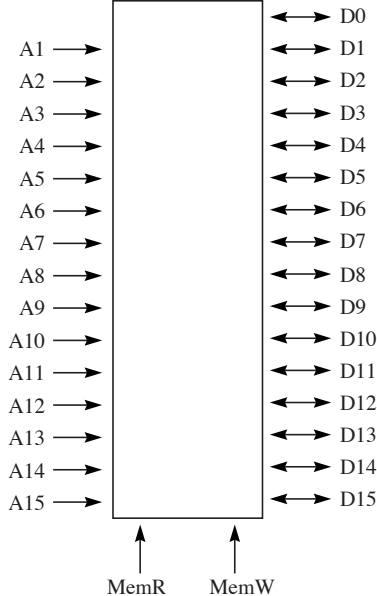


(a) The chip of Figure 12.2.



(b) A chip with a 16-bit data bus.