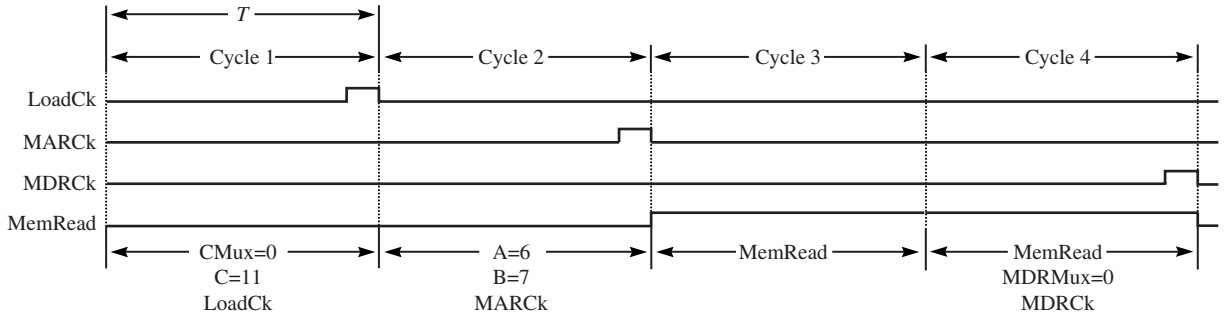
T
Cycle 1
Cycle 2
Cycle 3
Cycle 4
LoadCk
MARCk
MDRCk
MemRead
CMux=0
C=11
LoadCk
A=6
B=7
MARCk
MemRead
MemRead
MDRMux=0
MDRCk