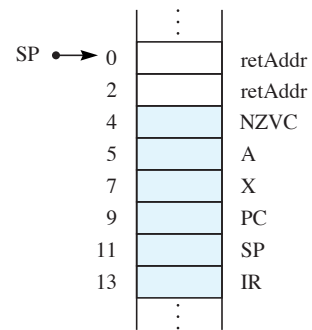


**(a)** Immediately after a trap.



**(b)** With two return addresses on the run-time stack.  
The shaded region is the PCB.