

Instruction Specifier	Instruction
0000 0000	Stop execution
0000 0001	Return from trap
0000 0010	Move stack pointer (SP) to accumulator (A)
0000 0011	Move NZVC flags to accumulator (A)
0000 010a	Branch unconditional
0000 011a	Branch if less than or equal to
0000 100a	Branch if less than
0000 101a	Branch if equal to
0000 110a	Branch if not equal to
0000 111a	Branch if greater than or equal to
0001 000a	Branch if greater than
0001 001a	Branch if V
0001 010a	Branch if C
0001 011a	Call subroutine
0001 100r	Bitwise invert register r
0001 101r	Negate register r
0001 110r	Arithmetic shift left register r
0001 111r	Arithmetic shift right register r
0010 000r	Rotate left register r
0010 001r	Rotate right register r
0010 01n	Unimplemented opcode, unary trap
0010 1aaa	Unimplemented opcode, nonunary trap
0011 0aaa	Unimplemented opcode, nonunary trap
0011 1aaa	Unimplemented opcode, nonunary trap
0100 0aaa	Unimplemented opcode, nonunary trap
0100 1aaa	Character input
0101 0aaa	Character output
0101 1nn	Return from call with n local bytes
0110 0aaa	Add to stack pointer (SP)
0110 1aaa	Subtract from stack pointer (SP)
0111 raaa	Add to register r
1000 raaa	Subtract from register r
1001 raaa	Bitwise AND to register r
1010 raaa	Bitwise OR to register r
1011 raaa	Compare register r
1100 raaa	Load register r from memory
1101 raaa	Load byte register r from memory
1110 raaa	Store register r to memory
1111 raaa	Store byte register r to memory